

ABSTRACT OF THE DISCLOSURE

An automatic circuit design apparatus includes an analyzer (11) for analyzing a file in a form of a table, in which both connection conditions concerning a plurality of circuit components (e.g., intellectual properties) within an integrated circuit and connections among the plurality of circuit components corresponding to the connection conditions are described, and a description creating unit (12) for creating a description of the integrated circuit according to HDL based on analytical results from the analyzer (11).